

[illegible]

The schematic diagram shows the following connections:

- J6:**
 - Pin 1: 3.3V
 - Pin 2: SF/BAL
 - Pin 3: ADC0
 - Pin 4: ADC
 - Pin 5: ADC
 - Pin 6: GND
- R14:** A 10k resistor connected between SF/BAL (J6 pin 2) and GND (J6 pin 6).
- JP1:** A jumper connecting SF/BAL (J6 pin 2) to ADC0 (J6 pin 3).
- JP2:** A jumper connecting ADC0 (J6 pin 3) to ADC1 (J6 pin 4).

J7
Patte de fixation

